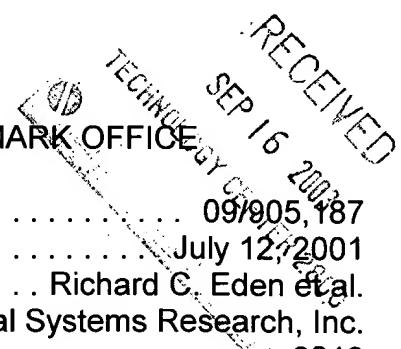




THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application Serial No. 09/905,487
Filing Date July 12, 2001
Inventor Richard C. Eden et al.
Assignee Isothermal Systems Research, Inc.
Group Art Unit 2812
Examiner D. Farahani
Attorney's Docket No. IS9-018
Confirmation No. 3067
Customer No. 021567
Title: Power Semiconductor Switching Devices, Power Converters, Integrated Circuit Assemblies, Integrated Circuity, Power Current Switching Methods, Methods of Forming a Power Semiconductor Switching Device, Power Conversion Methods, Power Semiconductor Switching Device Packaging Methods, and Methods of Forming a Power Transistor

INTERVIEW SUMMARY

Sir:

Applicants wish to thank the Examiner for the courtesies extended to the undersigned during the telephone interview. The undersigned presented distinctions of the subject matter of claims 28, 29 and 31 with respect to the teachings of the prior art. The distinctions were favorably received by the Examiner. Accordingly, Applicants hereby include an amendment wherein claim 1 is amended to include the limitations of 29. Applicants further include an amendment to claim 12 to include the limitations of claim 28. Applicants further include a new claim 37 including the limitations of claim 31 and claim 1.

Applicants respectfully request allowance of all pending claims.

The Examiner is requested to phone the undersigned if the Examiner believes such would facilitate prosecution of the present application. The undersigned is available for telephone consultation at any time during normal business hours (Pacific Time Zone).

Respectfully submitted,

Dated: 9/10/03

By:


James D. Shaurette
Reg. No. 39,833



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/905,187
Filing Date July 12, 2001
Inventor Richard C. Eden et al.
Assignee Isothermal Systems Research, Inc.
Group Art Unit 2812
Examiner D. Farahani
Attorney's Docket No. IS9-018
Confirmation No. 3067
Customer No. 021567
Title: Power Semiconductor Switching Devices, Power Converters, Integrated Circuit Assemblies, Integrated Circuitry, Power Current Switching Methods, Methods of Forming a Power Semiconductor Switching Device, Power Conversion Methods, Power Semiconductor Switching Device Packaging Methods, and Methods of Forming a Power Transistor

RESPONSE TO JUNE 10, 2003 OFFICE ACTION

To: Mail Stop Fee Amendment
Assistant Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

From: James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)
Wells, St. John, P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

AMENDMENTS